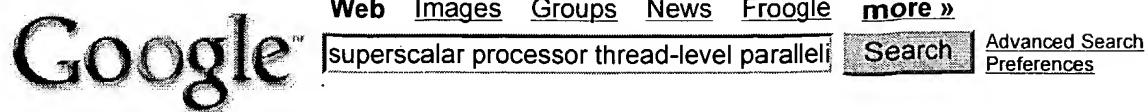


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7	6	multi\$thread\$1 near processor\$1 same execut\$4 same decode\$2	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/17 10:36
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12	52	super\$scalar near processor\$1 same decode\$2 same execut\$4 near units	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/17 10:38
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-	2	(ILP sane SMP same alternatively) and (storage with (buffer\$1 or locations) same thread\$1 same data)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/15 12:51
-	1	(ILP sane SMP same super\$scalar near2 processor\$1) and (storage with (buffer\$1 or locations) same thread\$1)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/15 12:52

	71	(ILP sane SMP same super\$scalar near2 processor\$1) and (storage with (buffer\$1 or locations) same data)	USPAT; US-PGPUB; EPO; JPO; IBM_TDB	2004/10/15 13:32
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